

A Novel Power Line to Reduce the Magnetic Field of Supply Currents in Josephson Digital Circuits

Yuki Yamanashi, Theodore Van Duzer Life Fellow IEEE,
Nobuyuki Yoshikawa Member IEEE

Abstract— One of the important issues facing the operation of large-scale Josephson digital circuits is that the dc supply currents are large and produce magnetic fields that have adverse effects. In this study, we have evaluated a novel patterning scheme for a superconductive twisted-pair bias supply line, which can reduce the static magnetic fields which adversely affect Josephson digital circuits. Several circuits have been designed and tested to evaluate the effectiveness of the twisted-pair bias supply lines, using the 2.5 kA/cm² SRL standard niobium process. Our experimental results indicate that the twisted-pair bias supply line can reduce substantially the static magnetic fields generated by the bias current. And it is demonstrated that the bias margin of large-scale Josephson circuits can be improved by using the novel bias supply lines. We believe that use of the twisted-pair will contribute, along with other techniques such as burying the bias lines under a thick ground plane, to the solution for the bias-current induced dc magnetic fields.

Index Terms—Bias current, Josephson junction, Single flux quantum circuit, Static magnetic fields

Manuscript received 19 August 2008.

Y. Yamanashi and N. Yoshikawa are with Yokohama National University, Yokohama 240-8501, Japan (e-mail: yamanashi@ynu.ac.jp).

T. Van Duzer is with University of California Berkeley, CA 94720-1770, USA (e-mail: vanduzer@eecs.berkeley.edu)