



## CALL FOR PAPERS

### IMPORTANT DATES

**April 21, 2026**  
Abstract Submission Deadline

**May 12, 2026**  
Early Bird Registration Deadline

**THE 17<sup>TH</sup> IEEE WORKSHOP ON LOW-TEMPERATURE ELECTRONICS IS BEING PLANNED FOR 1-5 JUNE 2026 TO BE LOCATED IN SICILY, ITALY.**

The recent progress in cryogenics technologies, along with the increasing interest in space-based communication, gives a new light to low temperature electronics. The increasing interest in quantum technologies and nanodevices, most of which need low temperatures, are envisaged as the traditional area of interest of the WOLTE community.

This workshop series has a consistent policy of holding no parallel sessions. Therefore, its program is comprised of single technical sessions. This gives a unique opportunity to stimulate discussions among all the participants traditionally coming from a variety of semiconductor and superconductor-related research fields.

Following its tradition of receptiveness to the emerging research field, the 17<sup>th</sup> edition of WOLTE, along with the traditional semiconducting and superconducting low temperature electronics, will include new topics on quantum technologies.

Industrial and technological applications will receive particular attention in a dedicated and attractive exhibition area.

### TOPICS

- » Low-temperature electronic and memory circuits, components, and devices
- » Low-temperature quantum sensors
- » Novel low-temperature devices including superconducting diodes, nanobridges, cryotrons, spintronic, voltage-controlled devices, charge density wave devices, etc.
- » Advances in modeling of low-temperature devices and circuits
- » Advances in low-temperature circuit technologies including neuromorphic and ballistic circuits
- » Low-temperature electronics for qubit and sensor control and readout
- » Low-temperature electronics for low latency quantum error correction
- » Low-temperature electronics for space and high-energy physics applications
- » Advances in cryocooling systems for electronics
- » Hybrid technology integrating different types of low-temperature electronics in a single system
- » Integration of low-temperature electronics with photonic, spin-based quantum systems
- » Advances in cryopackaging including cryosystems for quantum computers at scale, dark matter search systems
- » Advances in flux trapping management, modeling, and shielding techniques
- » Low-temperature Quantum Metrology: electronics and material
- » Low-temperature electronic measurements
- » Artificial Intelligence for Low-Temperature Electronics
- » Materials for low-temperature electronics

Submit an abstract of up to one page to the EDAS conference paper submission site. Additional details can be found at [2026.ieee-wolte.org](https://2026.ieee-wolte.org).

A Book of Abstracts will be prepared. In addition, WOLTE 2026 plans to offer the opportunity to participants to submit an expanded paper to selected IEEE Transactions journals.